

FIG. 1

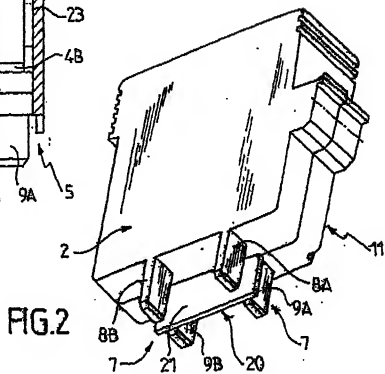


FIG. 2

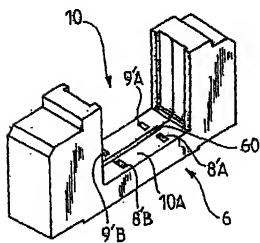
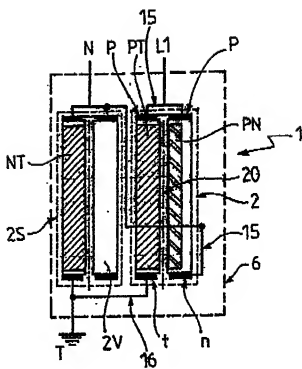


FIG. 3

FIG.4



This diagram shows a cross-sectional view of a semiconductor device. It features a substrate with a top layer labeled '6'. Below this, there are several vertical structures. The first structure on the left is labeled 'NT'. To its right are three pairs of vertical structures, each pair consisting of a shaded region and an unshaded region. These are labeled with 'N' and 'P' regions, and 'L1', 'L2', and 'L3' for the unshaded regions. The top of these structures is labeled '2A', '2B', and '2C'. The bottom of the structures is labeled '2S'. The substrate is labeled 'T' and '16'. The bottom of the substrate is labeled 't' and 'n'. The bottom of the structures is labeled 'PT' and 'PN'. The bottom of the structures is labeled '30'.